



EXPRESS MAIL NO. EV207743927US

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Davide Patti
Application No. : 09/733,781
Filed : December 7, 2000
For : HIGH-VOLTAGE INTEGRATED VERTICAL RESISTOR AND
MANUFACTURING PROCESS THEREOF

Examiner : Matthew E. Warren
Art Unit : 2815
Docket No. : 854063.601
Date : November 5, 2002

Commissioner for Patents
Washington, DC 20231

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AMENDMENT

Commissioner for Patents:

In response to the Office Action dated May 6, 2002, please extend the period of time for response three months, to expire on November 6, 2002. Enclosed are a Petition for an Extension of Time and the requisite fee. Please amend the application as follows:

In the Claims:

Please cancel claims 8-19.

Please add new claims 29-30 to read as follows:

29. The device of claim 1 wherein the resistor is a doped semiconductor region of the body that is laterally surrounded by an insulating region extending longitudinally into the body from a surface of the body, the insulating region including insulating walls made of electrically insulating material and a conductive filler that is laterally surrounded by the insulating walls.

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